

END USER LICENCE AGREEMENT FOR THE CORTEX MICROCONTROLLER SOFTWARE INTERFACE STANDARD (CMSIS) DELIVERABLES

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"CMSIS-CORE" means the specification defining the application programming interface, naming and coding conventions for the Cortex-M processor cores.

"CMSIS-DRIVER" means the specification defining a generic peripheral driver application programming interface, naming and coding conventions.

"CMSIS-DSP" means the digital signal process (DSP) library specification defining the application programming interface of a DSP library implementation.

"CMSIS-PACK" means the specification defining a software pack file format and the associated XML schema file.

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"Separate Files" means the components in the CMSIS reference implementation identified in the Schedule that demonstrate the usage of the CMSIS-CORE, CMSIS-DRIVER, CMSIS-DSP, CMSIS-PACK and CMSIS-RTOS API, for microprocessors or device specific software applications that are for use with microprocessors.

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SCHEDULE

Separate Files

The package also includes the components contained in the following directories:

- (a) ./CMSIS/DSP_Lib - DSP Library sources and examples;
- (b) ./CMSIS/Include - Header files;
- (c) ./CMSIS/Lib - DSP Library build for various toolchains;
- (d) ./CMSIS/RTOS - Header file template for CMSIS-RTOS implementation; and
- (e) ./Device - Template files and implementations for Cortex-M class processors.

All of the above components (a– e) are licensed to you under the terms of the BSD licence, which is incorporated within or alongside the above components.

- (f) ./CMSIS/Driver – CMSIS-Driver header files
- (g) ./CMSIS/Pack – Example Device Family Pack

The above components (f – g) are licensed to you under the terms of the zlib licence, which is incorporated within or alongside the above components.

ARM contract reference LEC-PRE-00489- v2.0